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A Garcia-Molina, P. C. ... - *Computing*, 1994 - [computer](#) ... We want to stress that this situation will only happen if the **operating system** provides the ISR with an ... of the task or the copy of the data (or both) can be deferred to the context **switch**. ... Kernel I Send-unlock 1 * - - time -t (**without context switch**) * Figure 9. Message-Passing 4. ...

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P Parsons ... - *US Patent App. 10096,881, 2002* - [Google Patents](#) ... registers are updated when work has been added to the circular buffers (this doesn't require any OS intervention). When a circular buffer goes empty the hardware will automatically search the pool of circular buffers for more work and instigate a context **switch** if necessary. ...

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B Bhargava, E. Mafra ... - *Department of Computer Sciences Purdue ...*, 1990 - [Elsevier](#) ... Now, the user can use the new **operating system** feature by invoking the corresponding Push routine with a special Page 4. ... the user level as a separate server process. Note the context **switch** overhead introduced by the frequent need to cross the user-kernel boundary. ...

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MS Lee, Y I Kang, JW Lee ... - *Microprocessors and ...*, 2002 - [Elsevier](#) ... In case of 16K context **switch** interval, the saturation point is 12K bits, and **without context switch** the saturation point is above 192K bits ... View Within Article. After OS finishes handling context **switch** including setting up PSR and SRC queue, process P3 starts to execute. ...

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B Bhargava, E. Mafra ... - *Journal of Systems Integration*, 1993 - [Springer](#) ... domains. **Switching** between domains significantly increases the cost of the services. Push can be used in a small kernel to supply crucial **operating system** services **without context switch** and protection domain overhead. The ...

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... The purpose of this report is to investigate some aspects of using commercial off-the-shelf (COTS) real-time **operating system** (RTOS) software that may affect safety in aviation systems. ... Known and predictable timing behavior (eg. interrupt latencies, task **switch** latencies, and ...

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... 475 Page 3. Care must be taken to insure that the application code operates as desired after this **switch** is enabled. e.) A change in underlying **operating system**. If the underlying **operating system** changes, this is essentially a change in the RTE. ...

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M. Fording, ... - US Patent App. US612,752, 2001 - Google Patents

... exclusion semaphore may not be given during an interrupt service routine, a special procedure used to handle hardware interrupts **without context switch**-ing. ... The **operating system** may prevent access to a resource by tasks that do not currently hold the associated semaphore. ...

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